

3. (Amended) The current source switching circuit according to claim 2, further comprising:  
a second load connected between a ground and a second side of said transistor switch.

4. (Amended) The current source switching circuit according to claim 3, wherein:  
said first load is a charging capacitor.

17. (Three Times Amended) The current source switching circuit according to claim 16, wherein [said current source switching circuit comprises]:  
said first load is a charged capacitor.

#### REMARKS

By this Amendment, it is proposed that the Drawings and Specification be amended to overcome the Examiner's objections. Claims 1-22 remain pending in the application.

The Applicants respectfully request the Examiner to reconsider his earlier rejections in light of the amendments and the following remarks. No new issues are raised nor is further search required as a result of the changes made herein. Entry of the Amendment is respectfully requested.

#### Objection to the Specification

The Examiner made an objection to the disclosure, page 9, because the disclosure allegedly did not detail "what the various labels within the equations represent".

The Specification is amended herein at page 9 in accordance with standard engineering equations representing the amount of charge in the inversion layer, and therefore do not constitute new matter. In particular,  $W$  represents the width of the inversion layer.  $L$  represents the length of the inversion layer.  $C_{ox}$  represents the capacitance of a unit area of the oxide layer.